

9-29-03



Attorney Docket No. MTI-31607

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Washington, D.C. 20231 SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (SEPT. - 2003)	ATTY. DOCKET NO.	Serial No.
	MTI-31607	10/050,507
	Applicant	Confirmation No.
	Lee, Teck Kheng	7687
	Filing Date	Group Art Unit
	January 16, 2002	2813

U.S. PATENT DOCUMENTS

Examiner Initials	Patent Number	Issue Date	Patentee	U.S. Class	Sub-Class	Filing Date
CAT	5,148,265	09/15/92	Khandros			
CAT	5,346,861	09/13/94	Khandros			
CAT	5,347,159	09/13/94	Khandros			
CAT	5,679,977	10/21/97	Khandros			
CAT	5,683,942	11/04/97	Kata			
CAT	5,777,391	07/07/98	Nakamura			
CAT	5,905,303	05/18/99	Kata			
CAT	6,217,343	04/17/01	Okuno			

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Examiner Initials	Document Number	Publication Date	Country	Int'l Class	Sub-Class	Translation (Yes/No)
CAT	EP684644	11/29/95	EPO			
CAT	EPI009027	06/14/00	EPO			
CAT	KR 2001054744	07/02/01	Korea			English Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initials	Non-Patent Document

Examiner Initials	<i>Cat 9-28</i>	Date Considered	<i>4-2-03</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

4-25-03



U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
Washington, D.C. 20231

**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(04-2003)**

ATTY. DOCKET NO.

MTI-31607

Applicant

Lee, Teck Kheng

Filing Date

01-16-02

Serial No.

10/050,507

Confirmation No.

7687

Group Art Unit

2813

U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Issue Date	Patentee	U.S. Class	Sub-Class	Filing Date

PUBLISHED U.S. PATENT APPLICATIONS

Examiner Initial	Publ. Number	Publ. Date	Applicant	U.S. Class	Sub-Class	Filing Date
CA	US 2002/0142513	Oct. 3, 2002	Fee et al.	438	106	Apr. 19, 2001

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Examiner Initial	Document Number	Publication Date	Country	Int'l Class	Sub-Class	Translation (Yes/No)

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial	Non-Patent Document

Examiner Initials <i>CA</i>	Date Considered <i>4-25</i>
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3-12-02

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Washington, D.C. 20231 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.	Serial No.
	MTI-31607	10/050,507
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	Lee, Teck Kheng	7687
	Filing Date	Group Art Unit
	01-16-02	2812

U.S. PATENT DOCUMENTS

		Patent Number	Issue Date	Patentee	U.S. Class	Sub-Class	Filing Date
CAT	A1	5,710,071	01-20-98	Beddingfield et al.	438	108	12-04-95
CAT	A2	6,048,755	04-11-00	Jiang et al.	438	118	11-12-98
CAT	A3	6,177,723	01-23-01	Eng et al.	257	691	12-17-97
CAT	A4	6,222,265	04-24-01	Akram et al.	257	723	12-17-99
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CAT	A9	6,295,730	10-02-01	Akram	29	843	09-02-99

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Examiner Initial	Document Number	Publication Date	Country	Int'l Class	Sub-Class	Translation (Yes/No)
B1						

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial	Non-Patent Document
C1	

Examiner Initials <u>Craig A. Z...</u>	Date Considered <u>4-4-5</u>
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2-8-05

Patent

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Examiner Initials	Patent Number	Publication Date	Patentee or Applicant	U.S. Class	Sub-Class
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CAT	5,812,378	09/1998	Fjelstad et al.		
CAT	5,892,271	04/1999	Takeda et al.		
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Examiner Initials	Patent Number	Publication Date	Patentee or Applicant	U.S. Class	Sub-Class
CAT	2004/0026773	02/2004	Koon et al.	<div style="text-align: center;"> RECEIVED FEB 08 2005 OFFICE OF PETITIONS </div>	
CAT	2004/0212055	10/2004	Exposito et al.		
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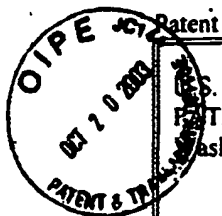
Examiner Initials	Document Number	Publication Date	Country	Name of Patentee or Applicant	Translation (Yes/No)
CAT	JP 04030456	02/1992	Japan	Yamaguchi	Yes (Abstract)
CAT	EP 0475022 A1	03/1992	EPO	Grube et al.	
CAT	EP 0997942 A2	05/2000	EPO	Horiuchi et al.	
CAT	JP 02000230964	08/2000	Japan	Hikita	Yes (Abstract)

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initials	Non-Patent Document
CAT	Australian Search Report dated 11 Aug 2004 (3 pages).
CAT	Australian Search Report dated 16 Aug 2004 (4 pages).

Examiner Initials <u>Craig A. Jell</u>	Date Considered <u>4-4-5</u>
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10-20-03



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CA	6,413,102	July 2, 2002	Jiang et al.	439	70	Dec.22, 1999

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Examiner Initials	Document Number	Publication Date	Country	Int'l Class	Sub- Class	Translation (Yes/No)

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initials	Non-Patent Document

Examiner Initials	<i>Chen A-20</i>	Date Considered	<i>4-4-5</i>
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FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Examiner Initial		Document Number	Publication Date	Country	Int'l Class	Sub-Class	Translation (Yes/No)
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CAT	A2	2001077294	03-23-01	JP	H01L	25/065	Yes
CAT	A3	99/65282	12-16-99	WO	H05K	1/11	N/A

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

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CAT	B2	Al-sarawi, S. et al., "A Review of 3-D Packaging Technology," <i>Components, Packaging, and Manufacturing Technology, Part B: IEEE Transactions on Advanced Packaging</i> , Vol 21, Issue 1, Feb. 1998, pp. 2-14.
CAT	B3	Andros, F. et al., "TBOA Package Technology," <i>Components, Packaging, and Manufacturing Technology, Part B: IEEE Transactions on Advanced Packaging</i> , Vol. 17, Issue 4, Nov. 1994, pp. 564-568.
CAT	B4	Clot, Ph. et al., "Flip-Chip on Flex for 3D Packaging," 1999. 24 th IEEE/CPIAT, 18-19 Oct. 1999, pp. 36-41.
CAT	B5	Ferrando, F. et al., "Industrial Approach of a Flip-Chip Method Using the Stud-Bumps With a Non-Conductive Paste," <i>Adhesive Joining and Coating Technology in Electronics Manufacturing</i> , 2000. Proceedings. 6 th International Conference on, 18-21, June 2000, pp. 205-211.
CAT	B6	Gallagher, C. et al., "A Fully Additive, Polymeric Process for the Fabrication and Assembly of Substrate and Component Level Packaging," <i>The First IEEE International Symposium on Polymeric Electronics Packaging</i> , 26-30, Oct. 1997, pp. 56-63.
CAT	B7	Geissinger, J. et al., "Tape Based CSP Package Supports Fine Pitch Wirebonding," <i>Electronics Manufacturing Technology Symposium</i> , 2002, IEMT 2002, 27 th Annual IEEE/SEMI International, 17-18 July 2002, pp. 41-452.
CAT	B8	Hatnaka, H., "Packaging Processes Using Flip Chip Bonder and Future Directions of Technology Development," <i>Electronics Packaging Technology Conference</i> , 2002. 4 th , 10-12, Dec. 2002, pp. 434-439.
CAT	B9	Haug, R. et al., "Low-Cost Direct Chip Attach: Comparison of SMD Compatible FC Soldering with Anisotropically Conductive Adhesive FC Bonding," <i>IEEE Transactions on Electronics Packaging Manufacturing</i> , Vol. 23, No. 1, Jan 2000, pp. 12-18.

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09-04-2004

Patent

Journey Docket No. MTI-31607

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Alexandria, VA 22313 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.	Serial No.
	MTI-31607	10/050,507
	Applicant	Confirmation No.
	Tock Kheng Lee	7687
	Filing Date	Group Art Unit
	January 16, 2002	2813

Examiner Initial		Non-Patent Document
COT	B10	Isak, H. et al., "Development of Flex Stackable Carriers" IEEE Electronic Components and Technology Conference, 2000 Proceedings 50th, 5/21/2000-5/24/2000, Las Vegas, NV, USA, pp. 378-34, IEEE Catalog No: 00CH37070.
LAT	B11	Kloeser, J. et al., "Fine Pitch Stencil Printing of Sn/Pb and Lead Free Solders for Flip Chip Technology," <i>IEEE Transactions of CPMT - Part C</i> , vol. 21, No. 1, 1998, pp. 41-49.
LAT	B12	Kheng et al., "Enhancement of Moisture Sensitivity Performance of a FBGA," <i>Proceedings of International Symposium on Electronic Materials & Packaging</i> , 2000, pp. 470-475.
LAT	B13	Li, L. et al., "Stencil Printing Process Development for Flip Chip Interconnect," <i>IEEE Transactions Part C: Electronics Packaging Manufacturing</i> , Vol. 23, Issue 3, July 2000, pp. 165-170.
LAT	B14	Lyons, A. et al., "A New Approach to Using Anisotropically Conductive Adhesives for Flip-Chip Assembly, Part A," <i>IEEE Transactions on Components, Packaging, and Manufacturing Technology</i> , Vol. 19, Issue 1, March 1996, pp. 5-11.
LAT	B15	Too, Y. et al., "Enhancing Moisture Resistance of FBGA," <i>Electronic Components and Technology Conference</i> , 1998, 48 th IEEE, 25-28 May 1998, pp. 930-935.
LAT	B16	Teutsch, T. et al., "Wafer Level CSP using Low Cost Electroless Redistribution Layer," <i>Electronic Components and Technology Conference</i> , 2000, 2000 Proceedings, 50 th , 21-24 May 2000, pp. Pages: 107-113.
LAT	B17	"The 2003 International Technology Roadmap for Semiconductors: Assembly and Packaging", pp. 1-22
LAT	B18	Tsu, C. et al. "Pad Distribution Technology for Flip Chip Applications", <i>1998 Electronic Components and Technology Conference</i> , pp. 1098-1102
LAT	B19	Xiao, G. et al., "Reliability Study and Failure Analysis of Fine Pitch Solder-Bumped Flip Chip on Low-Cost Flexible Substrate Without Using Stiffener," <i>IEEE, 2002. Proceedings 52nd, 28-31 May 2002</i> , pp. 112-118.

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09-09-2004

Patent

Attorney Docket No. MTI-31607

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	Applicant	Confirmation No.
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing Date	Group Art Unit
	January 16, 2002	2813

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class
CAI	A1	3,239,496	03/1966	Jursich		
CAI	A2	4,074,342	02/1978	Horn et al.		
CAI	A3	4,807,021	02/1989	Okumura		
CAI	A4	4,818,728	04/1989	Rai et al.		
CAI	A5	5,366,794	11/1994	Nakao		
CAI	A6	5,385,869	01/1995	Lin et al.		
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CAI	A9	5,404,044	04/1995	Booth et al.		
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CAI	A11	5,422,205	06/1995	Inoue et al.		
CAI	A12	5,438,477	08/1995	Pasch		
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CAI	A15	5,489,804	02/1996	Pasch		
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CAI	A17	5,598,033	01/1997	Bohlen et al.		
CAI	A18	5,648,446	07/1997	Nicewarner, Jr. et al.		
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CAI	A21	5,674,785	10/1997	Akram et al.		
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Examiner Initials	Crav 9-20	Date Considered	4-4-5
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SEPT. 20, 2004

Patent

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Alexandria, VA 22313 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.	Serial No.
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	Tock Kheng Lee	7687
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Examiner Initial	Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class
AT	A35	5,834,338	11/1998	Takeda et al.	
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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Patent

Attorney Docket No. MTI-31607

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE Alexandria, VA 22313 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.	Serial No.
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	Tock Khing Lee	7687
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Examiner Initial		Document No.	Publication/ Issue Date	Patentee	U.S. Class	Sub-Class
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